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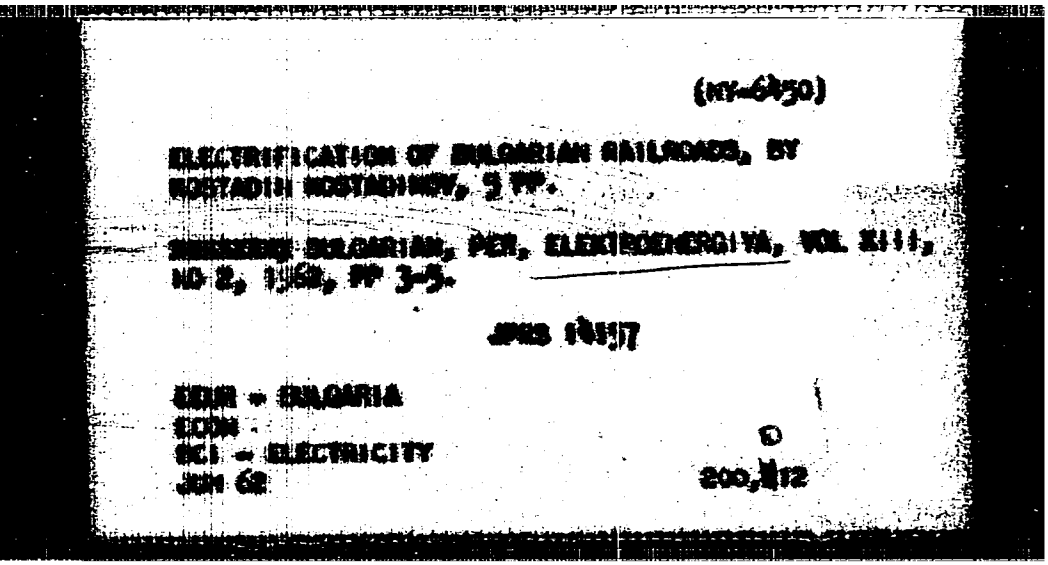

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USASIA: SC-166/60

International
Econ - Television
Jun 60

120,164

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USASIA
SC-167/168/169/170/172/60

Sep 61

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USASIA: SC-171/60

Worldwide

Econ

Jul 60

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USASIA: SC-173/60

[redacted]
International
Econ - Television
Jun 60

120,162

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USASIA: SC-174/60

WorldWide
Econ
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GERMAN, per, Das Elektron, No 6, 1959, pp 132-134, 147.

USASIA SC-175/60

EE
Sci - Electronics

Sep 60

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RMAN,
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9, 222, 223, 225, 226, 239, 240.

USASIA SC-176/60

i - Electronics

pp 60

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USASIA: SC-177/60

International
Econ - Radio and Television
Jun 60

120,167

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USASIA: SC-180/60

WorldWide
Econ - Television
Jun 60

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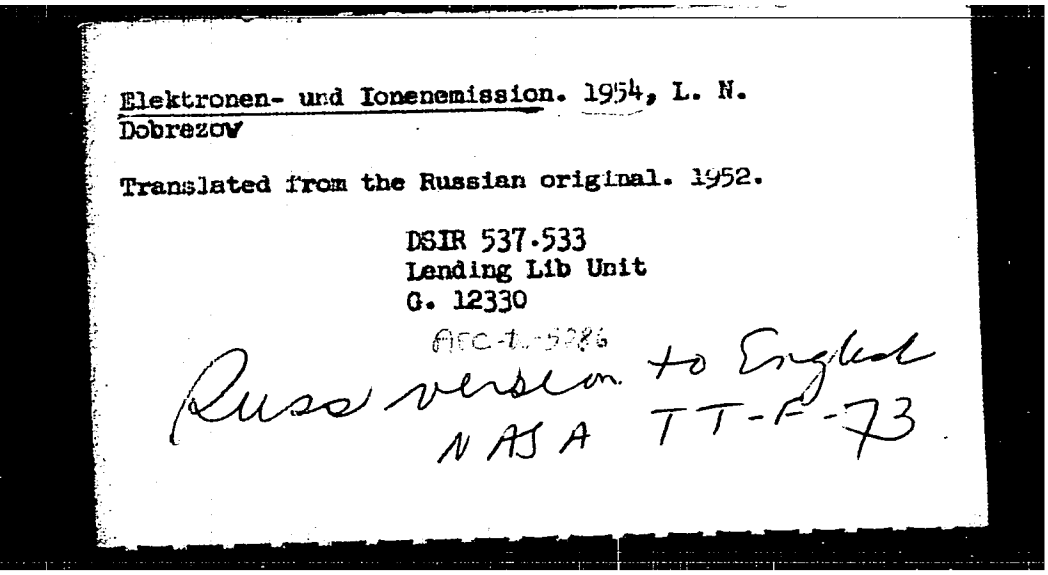

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DSIR 537.533
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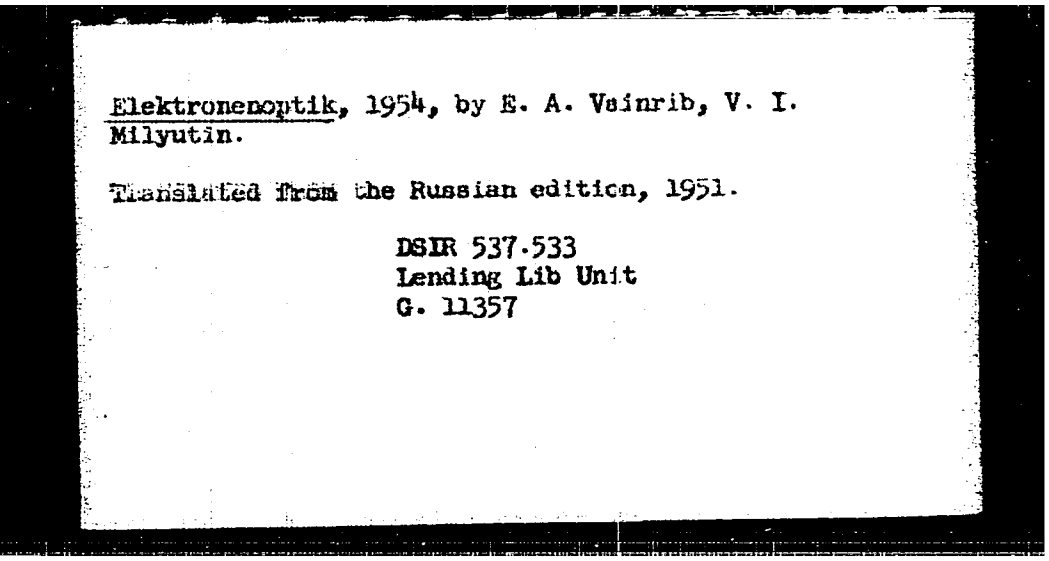

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Sci - Electronics 168,630

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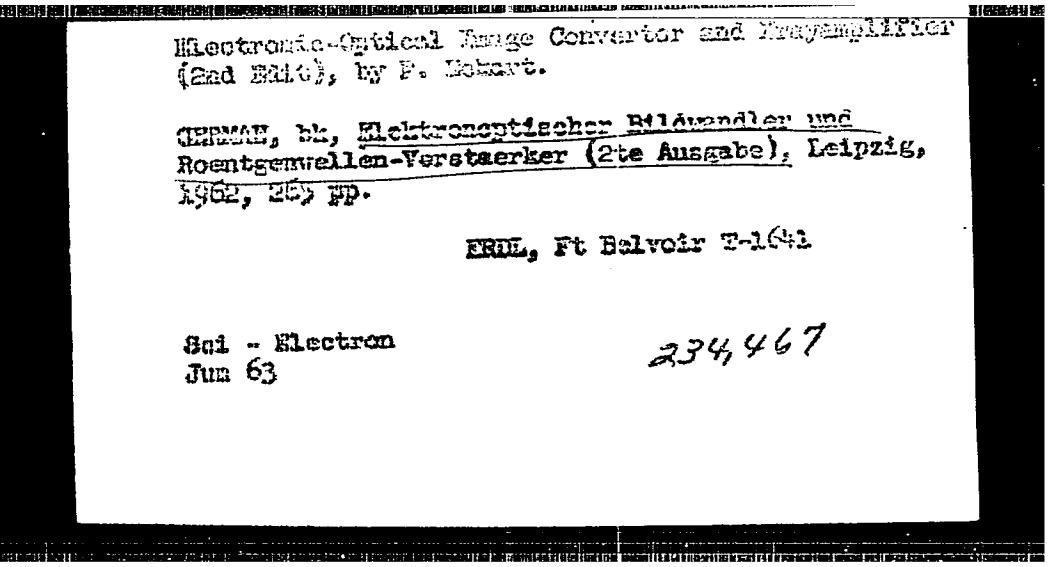

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A Cold-Cathode Ring Counter Circuit With Direct Numerical Indication, by H. Liebendorfer.

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Aschmoneit, Ernst-Karl.
SIMPLE BINARY COUNTER FOR ADDITION AND SUBTRACTION. [1963] 6p. (figs. omitted) 2 refs.
Order from SLA $1.10 63-18875

Trans. of Elektronik (West Germany) 1960 [v. 9] no. 8, p. 232-234.

DESCRIPTORS: *Computers, Computer logic, Design, Operation, Arithmetic operations.

Technical details are given of a binary counter made by Servomechanisms, Inc.

(Mathematics--Computing Devices, TT, v. 10, no. 11)

63-18875

I. Aschmoneit, E.-K.

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